		STUDY MODULE D	ES	CRIPTION FORM			
Name of the module/subject Switching processes in electric power networks					Code 1010311361010315997		
Field of study Electrical Engineering				Profile of study (general academic, practical) (brak)		Year /Semester 3 / 6	
Elective path/specialty				Subject offered in: Polish		Course (compulsory, elective)	
Cycle of study:			Forr	n of study (full-time,part-time)		
First-cycle studies				full-time			
No. of hours						No. of credits	
Lecture: 30	Classes	s: 15 Laboratory: -	ŀ	Project/seminars:	15	4	
Status of the course	in the study	program (Basic, major, other) (brak)	(1	iniversity-wide, from another	field) (bra	ak)	
Education areas and	fields of sci	ence and art				ECTS distribution (number and %)	
technical sciences						4 100%	
Techn	ical scie	ences				4 100%	
Responsible f	or subje	ect / lecturer:					
dr hab. inż. Ryszard Batura email: ryszard.batura@put.poznan.pl tel. 061 665 2767 Wydział Elektryczny							
ul. Piotrowo 3A	, 60-965 P	oznań					
Prerequisites in terms of knowledge, skills and social competencies:							
1 Knowle	dge	Fundamentals of the electrical d Knowledge. Knowledge of the si distribution system?s structure.	the electrical devices and measuring equipment and ots application. vledge of the single- and three-phase AC systems and the electric power m?s structure.				
2 Skills		Ability to acquire information from the literature in the field and other sources and to analyze it nevaluative way. Ability to deal with the analytical, simulation and experimental tools.					
		1c. Has understanding of the as decisions. Is able to work in the	spects and effects of the engineer?s responsibility for made team.				
3 Social compet	encies	Has basic knowledge of the construction solutions, parameters and choice criterions of electric power switches, MV switchgears, bus bars and bus ducts. Is able to construct the test networks and to carry out the electric power devices tests.					
Assumptions	and obj	ectives of the course:					
Getting familiar with switching phenomena under the normal and disturbed conditions; acquisition of skills to analyze it; getting familiar with the overvoltage limiters? design method and practice.							
Stud	y outco	mes and reference to the	edu	cational results fo	r a f	ield of study	
Knowledge:							
1. Has ordered and rules, knows basic power networks.	d theoretic features c [K_W04 +	ally underpinned knowledge of the of the electric circuits elements, ha ·+]	e ele as kn	ctric circuits theory, know owledge about the steady	s bas / and	sic electrical engineering?s transient states in electric	
2. Has knowledge power system eler	of the phe nents to a	nomena accompanying the switch nalyze the transient states [K_	ning p _W04	processes. Is able to cons ++]	struct	schemes of the electric	
3. Can find values of the expected recovery voltage, over-voltages, overcurrents during cut-off operation with and without the electric arc re-ignition for different DC and AC circuits - [K_W04 ++]							
4. Is familiar with methods of calculation and limiting the latter and can find it in analytical way - [K_W04 ++]							
Skills: 1. Can carry out an	nalysis of t	he simple electric system and dev	/ices	operation implementing	adeo	quate methods and tools	
 [K_U11 +] 2. To use data sheets and application notes to choose the proper elements of the electric network or system being designed. [K_U17 +] 							
<u>- [n_017 +]</u>	tencies:						

1. Has understanding of the need and knows opportunities of the lifelong learning (second and third cycle studies, postgraduate courses) as well as the need for upgrading the professional, personal and social competencies. Is able to think and act in the professional way. - $[K_{K01} +]$

Assessment methods of study outcomes

Lectures:

?Assessment of the knowledge and skills during the problem-type written examination,

?Continuous assessment, at each class (bonus for activity and perception quality).

Classes:

?Test and bonus for a knowledge necessary to solve tasks in the scope of the lectures? subjects.

?Assessment of the skills related to the class task accomplishment.

Projects:

?Test and bonus for a knowledge necessary to accomplish the design task,

?Assessment of the knowledge and skills related to the design task accomplishment.

Adding extra points for activity in discussions, especially for:

?effectiveness of implementation of the knowledge acquired when solving a given problem.

?ability to cooperate in the team accomplishing in practice a specific task in lab.

?remarks related to the educational materials? enhancement,

?care and esthetic form of the elaborated designs ? within the individual work.

Course description

DC cut off(breaking (the cut off conditions referring to the voltage-current curves and power balance for the constant and variable arc length, electric arc?s limit length, finding the arc duration and cut off operation in the analytical and graphical way) and description of influence of the circuit parameters and arc ignition manner on the switching overvoltages. Conditions of the AC current cut-off nearby the natural current-zero crossing and using step-up voltage. Post-arc end electric resistance of the contact gap for the short and long arcs. Development of the electric power network elements? scheme for the transient states analysis (equivalent schemes of generators, transformers, overhead and cable lines. Current and voltage transformers, bus bar). Finding overvoltages, overcurrents and recovery voltages during the fault clearance in the three-phase circuits with isolated and earthened neutral point; resistive load currents? switching; switching on and cut off of condensers with and without electric arc re-ignition; cut off of the low inductive currents and asynchronous motors. Overvoltage and overcurrent limiting methods. Discussions and design work with the lecture-related subjects.

Basic bibliography:

1. Królikowski Cz.: Inżynieria łączenia obwodów elektrycznych wielkich mocy, Wydawnictwo Politechniki Poznańskiej, 1998.

2. Królikowski Cz.: Technika łączenia obwodów elektroenergetycznych, WNT, Warszawa, 1990.

3. Bolkowski St.: Teoria obwodów elektrycznych, WNT, Warszawa, 1995.

Additional bibliography:

- 1. Magazins Elektroinstalator, Elektroinfo.
- 2. Related standards.
- 3. Manufacturers? data sheets.
- 4. Internet publications

Result of average student's workload

Activity	Time (working hours)	
1. Lectures		30
2. Classes	15	
3. Projects	15	
4. Part in consultations	30	
5. The preparation to occupations, the study of laboratory document	30	
Student's wo	rkload	
Source of workload	hours	ECTS
Total workload	120	4
Contact hours	90	3
Practical activities	30	1